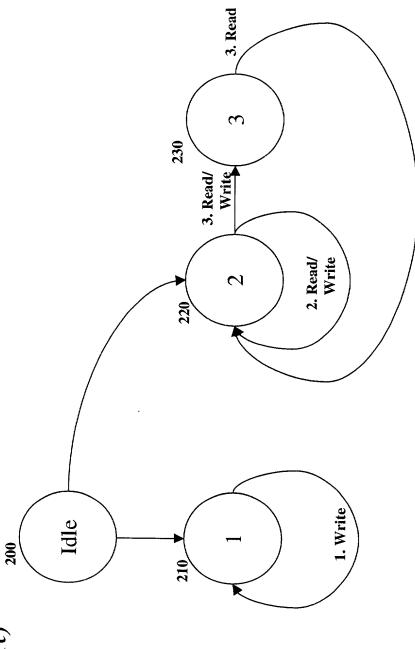
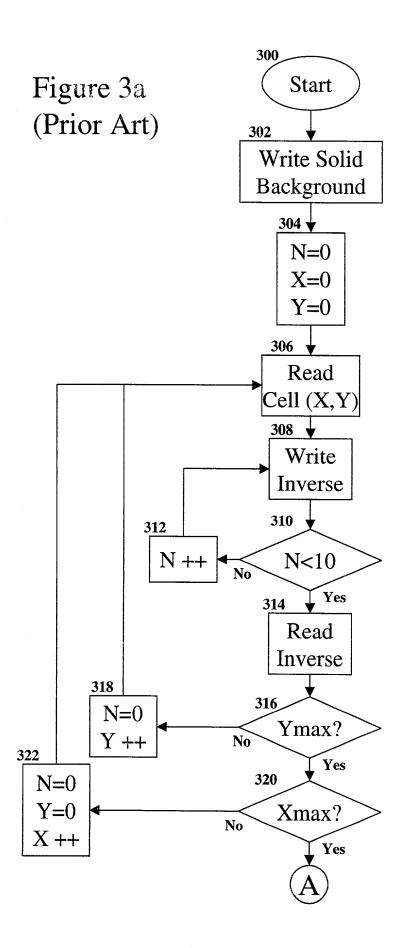
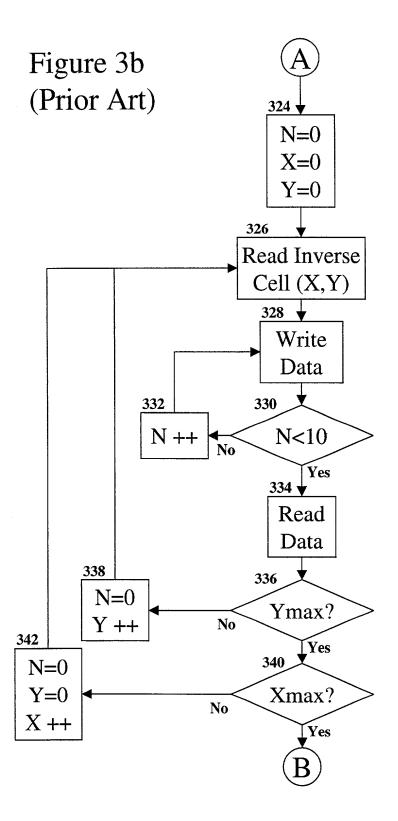
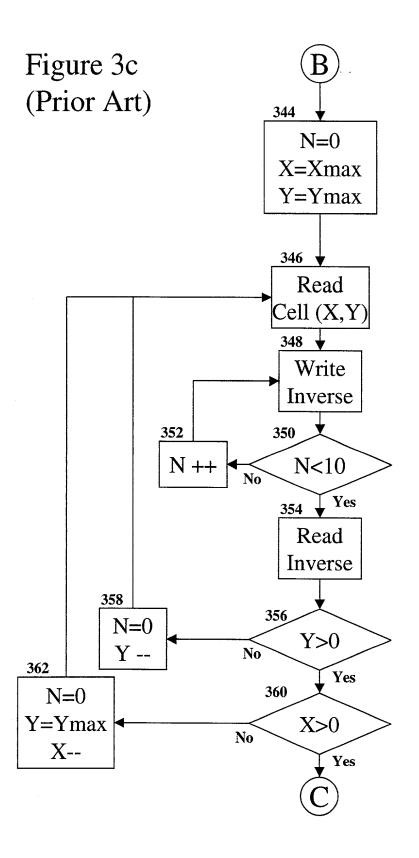


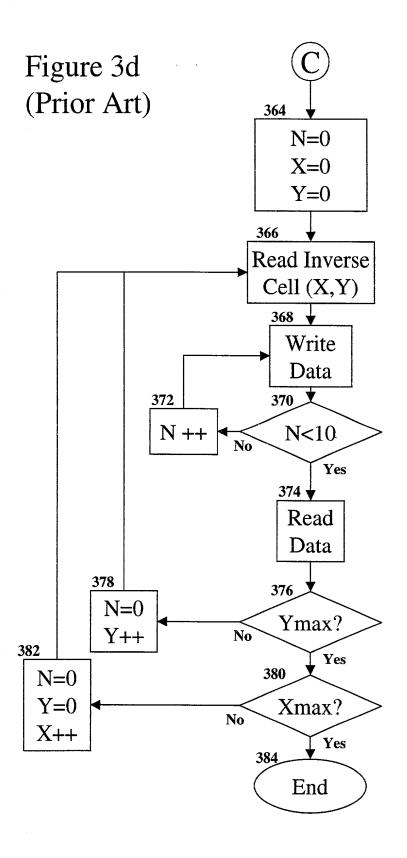
Fig. 2 (Prior Art)











2. Write 445 6 2. Write  $\infty$ 2. Write 435 \_ 2. Write 2. Write 425 S 2. Write 420 2. Read 415 3 (Prior Art) 1. Write Idle 400 405

465

2. Read

455

13

2. Write

2. Write

2. Write

2. Write

12

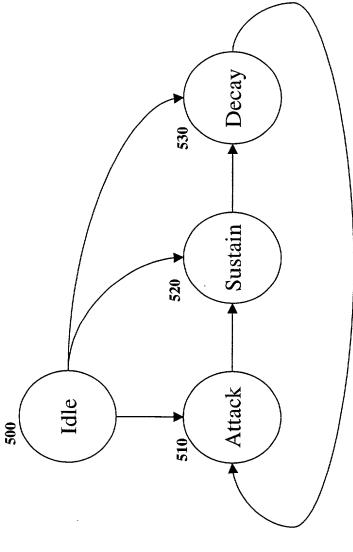
10

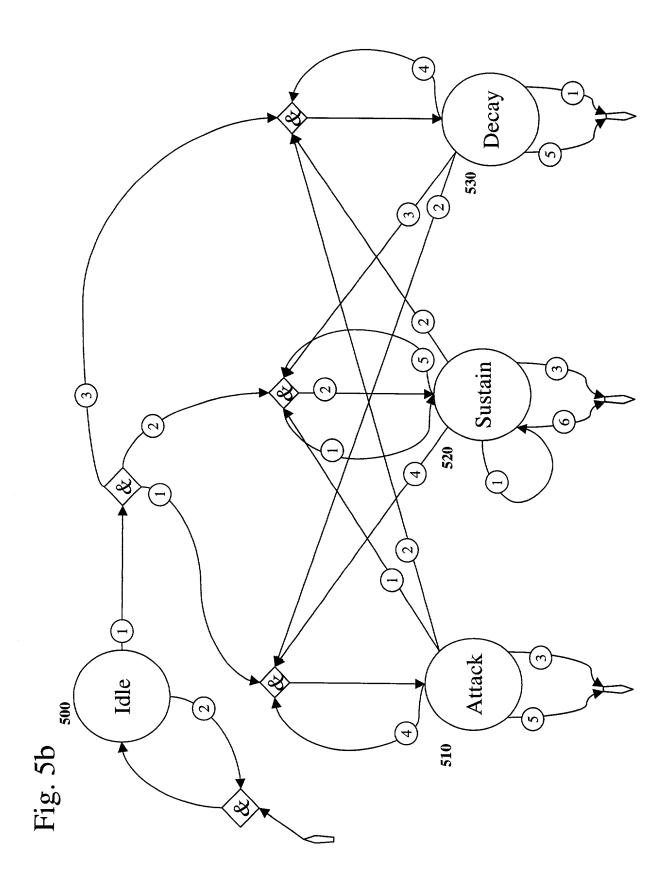
460

450

Fig. 4

Fig. 5a





(Addr3 Decay (Decay Addr2 A Decay Addr1 Addro] (Decay Fig. 6d State | Idle Addr ReadEn WriteEn Decay| Write (Sustain) NOP Fig. 6a Idle NOP 200

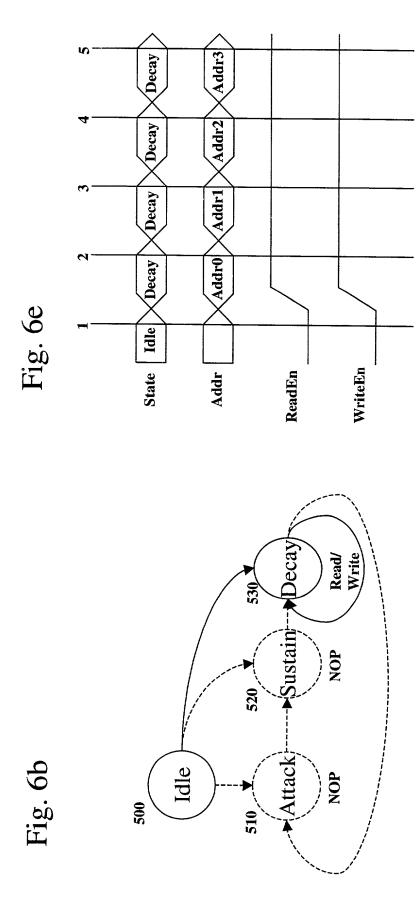
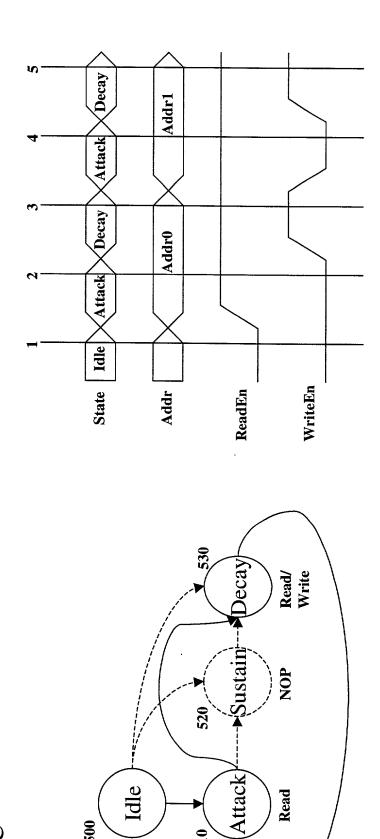


Fig. 6f Fig. 6c



200

Fig. 7a

Fig. 7b

